

10981866-1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Gupta et al.

Serial No.: 09/378,596

Filing Date: August 20, 1999

**AUTOMATIC DESIGN OF** 

PROCESSOR DATAPATHS

Examiner:

Makhdoom, S.

Art Unit:

2123

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DEC 1 1 2002

Technology Center 2100

**RESPONSE TO OFFICE ACTION** 

Hon. Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

In response to the Office Action mailed October 04, 2002, Applicants respectfully request reconsideration of the above referenced patent application. Please amend the above identified application as follows and consider the following remarks for allowance of the above identified patent application.

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Examiner: Makhdoom, S.

Serial No.: 09/378,596 Group Art Unit: 2123